



EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS  
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED IN CPU)

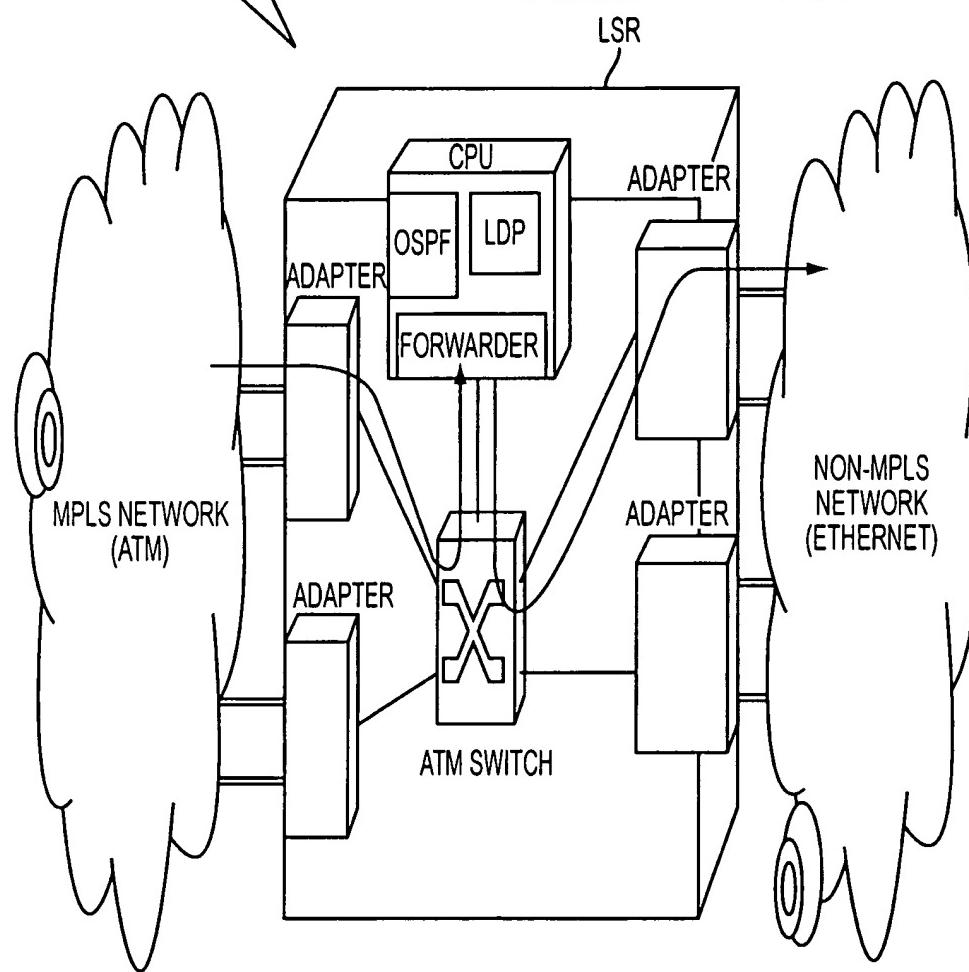
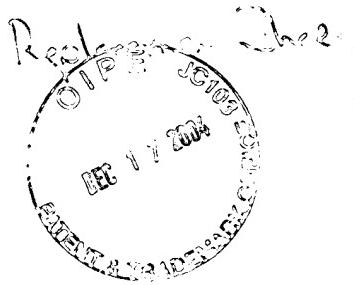


FIG. 1  
PRIOR ART



EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS  
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED AT PRE-STAGE  
OF CPU AND SHORT-CUTS CPU)

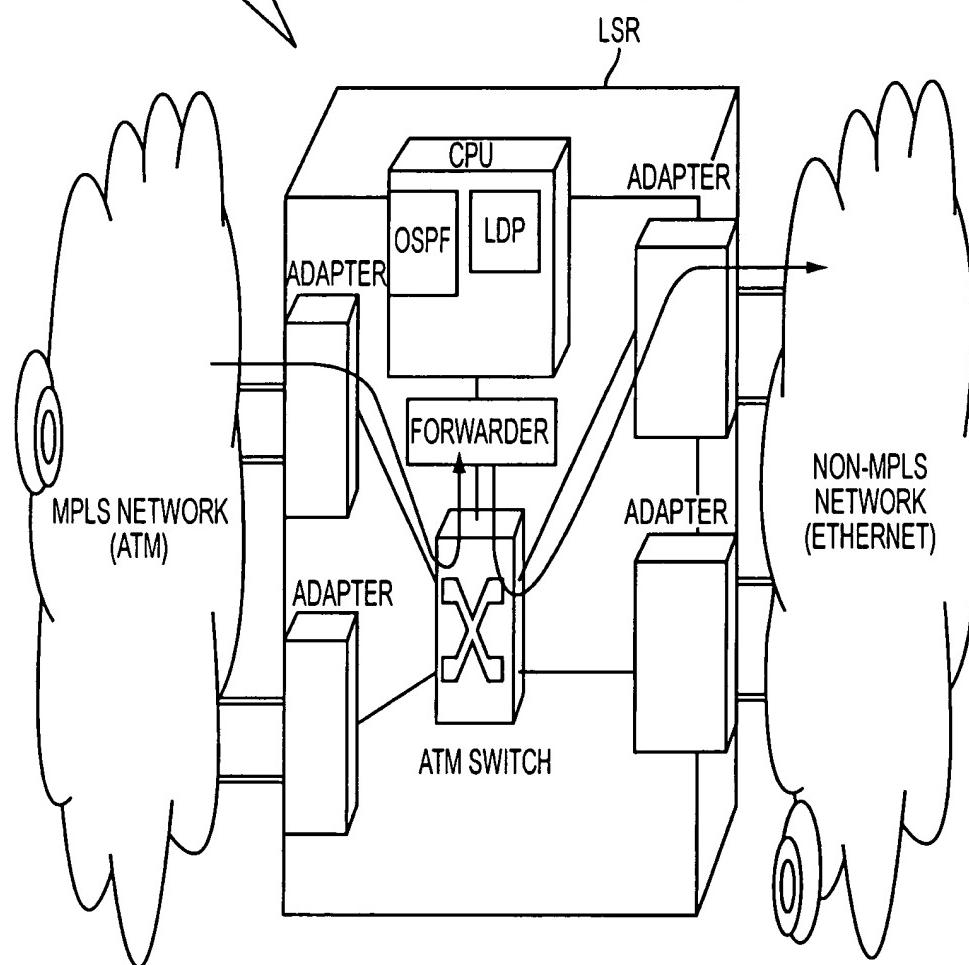
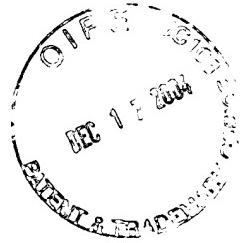


FIG. 2  
PRIOR ART



## EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED IN EACH ADAPTER)

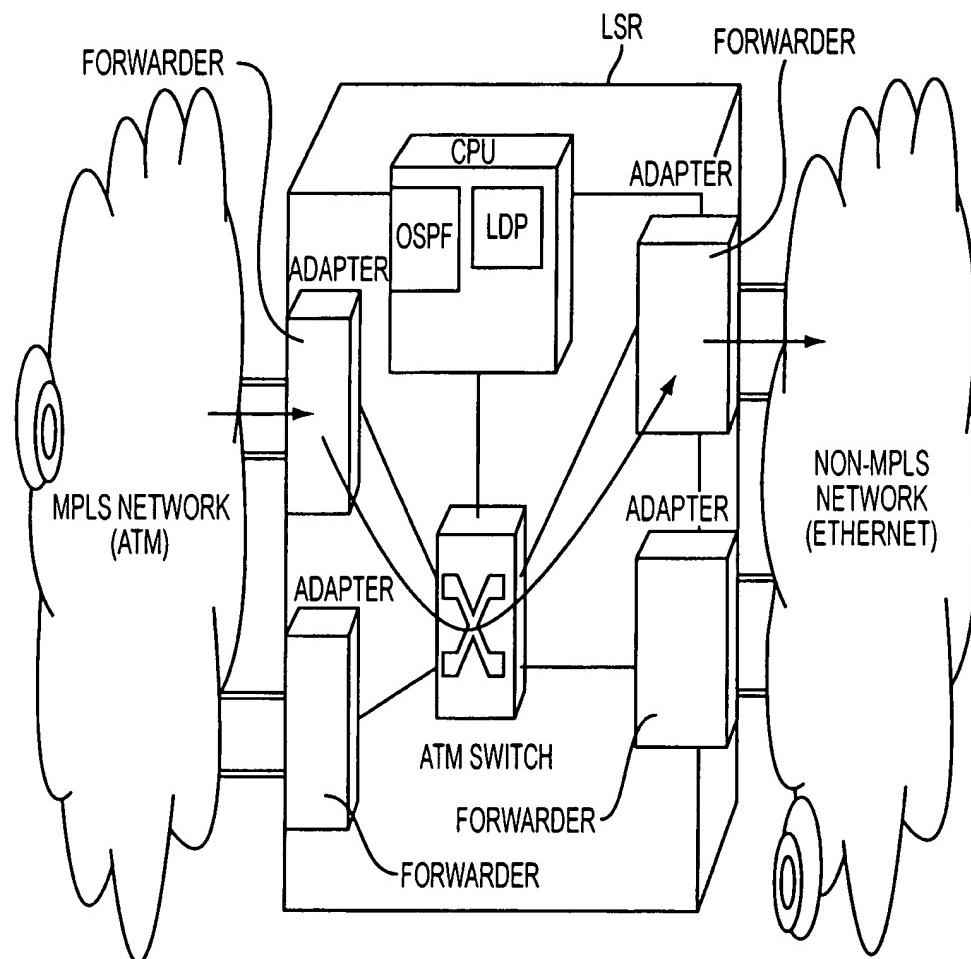
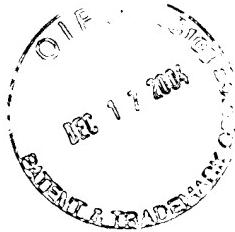


FIG. 3  
PRIOR ART

Replies due



EXAMPLE OF ARCHITECTURE OF LSR AS ATM-SWITCH BASE MPLS  
EDGE DEVICE (IP/MPLS FORWARDER IS MOUNTED IN ADAPTER  
AT EGRESS FOR PACKETS)

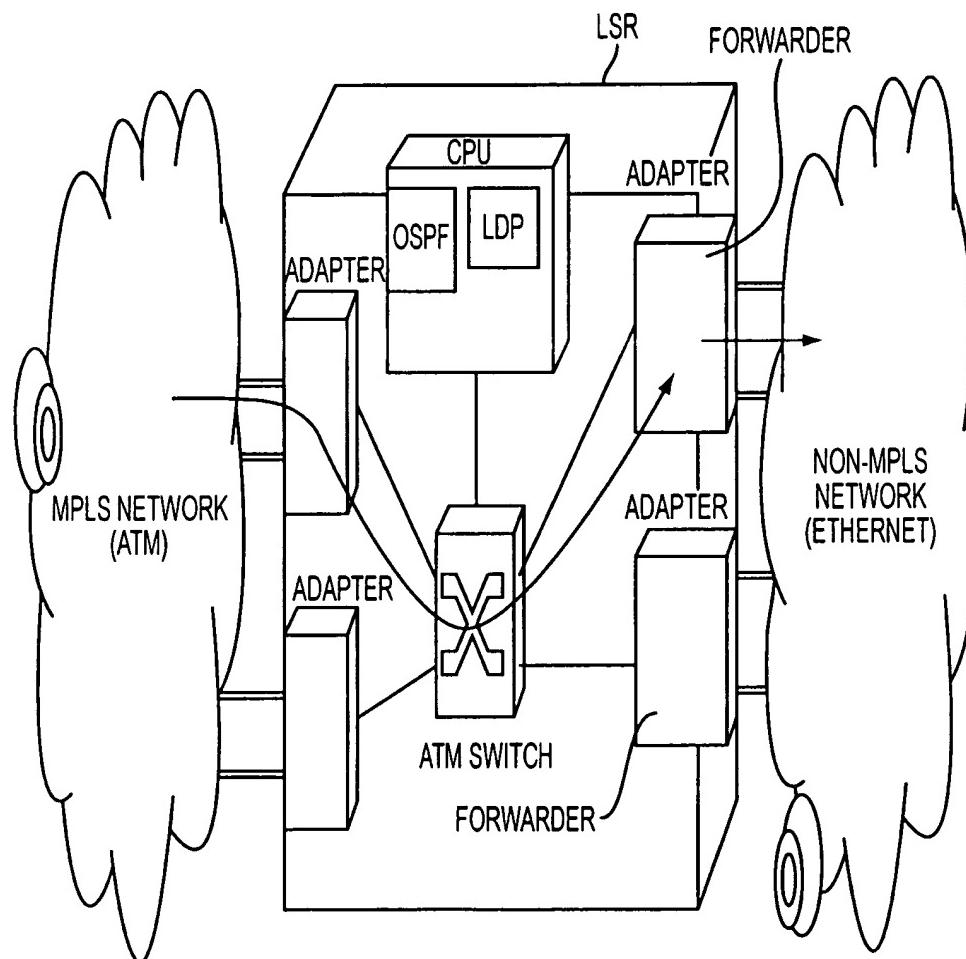
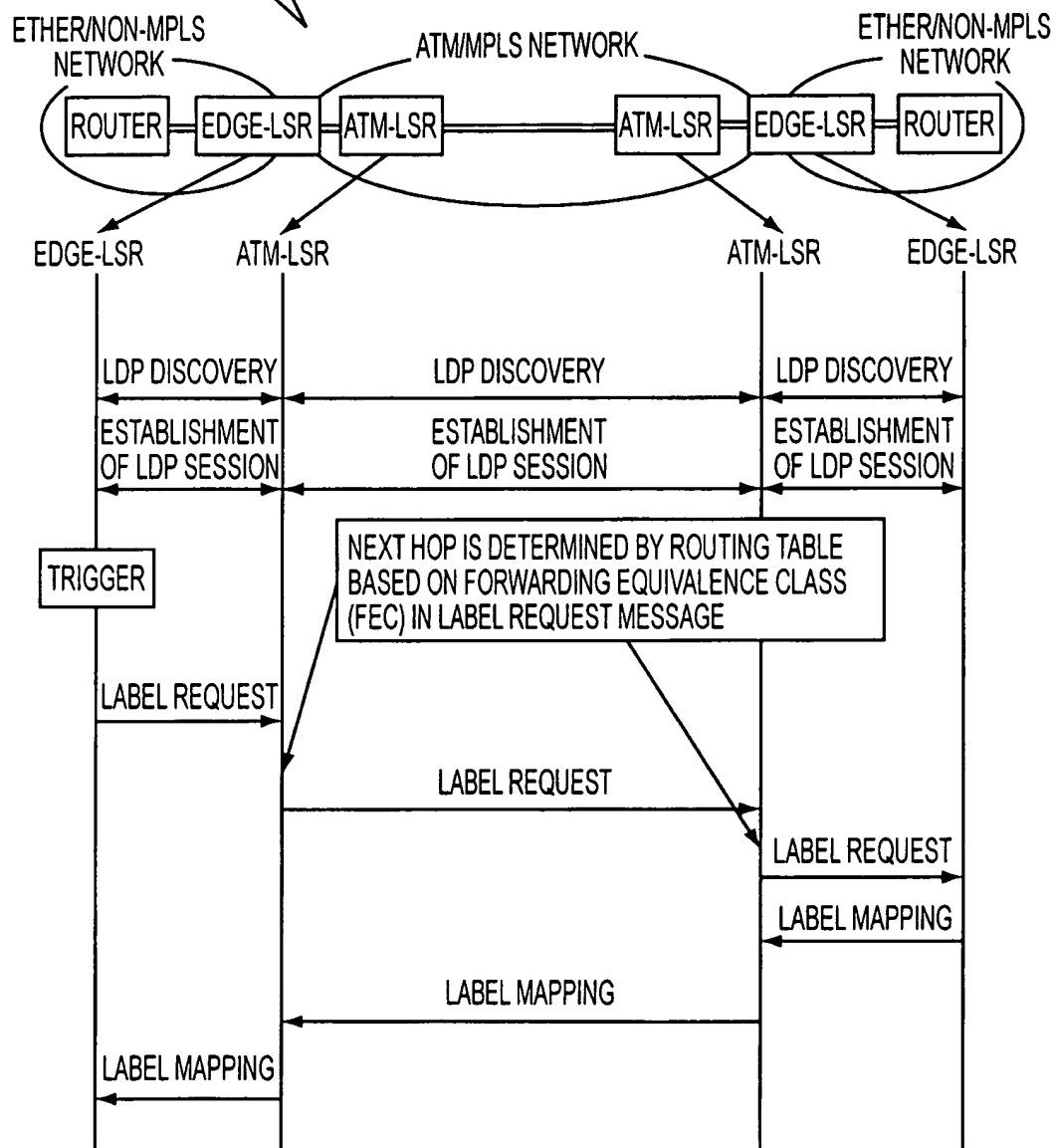


FIG. 4  
PRIOR ART



## LABEL DISTRIBUTION SEQUENCE BY HOP BY HOP ROUTING IN MPLS (LDP-BASED CASE: EXAMPLE OF DOWNSTREAM-ON-DEMAND/ ORDERED CONTROL)



**FIG. 5**  
**PRIOR ART**

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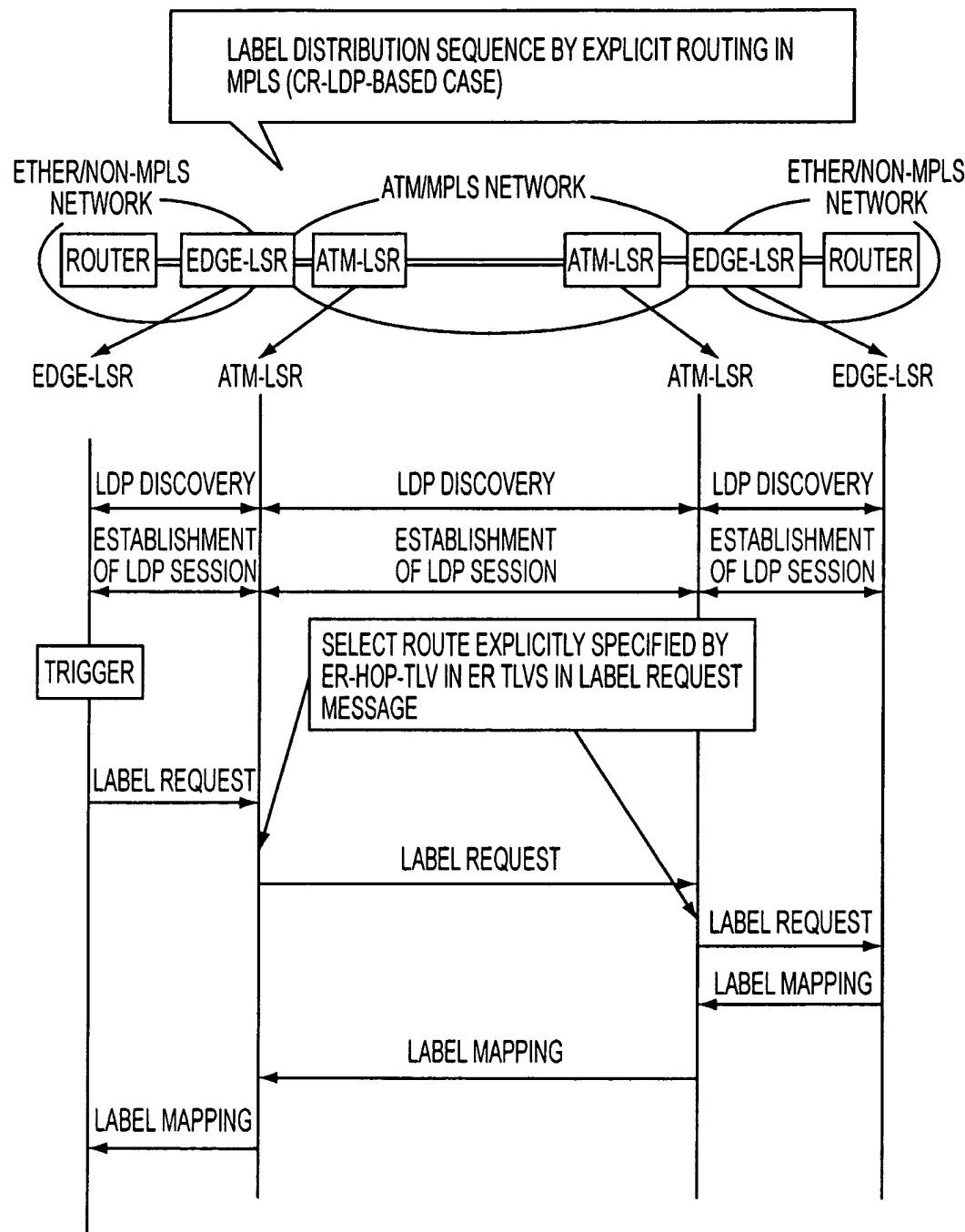


FIG. 6  
PRIOR ART

Replaced by Sec 9.1

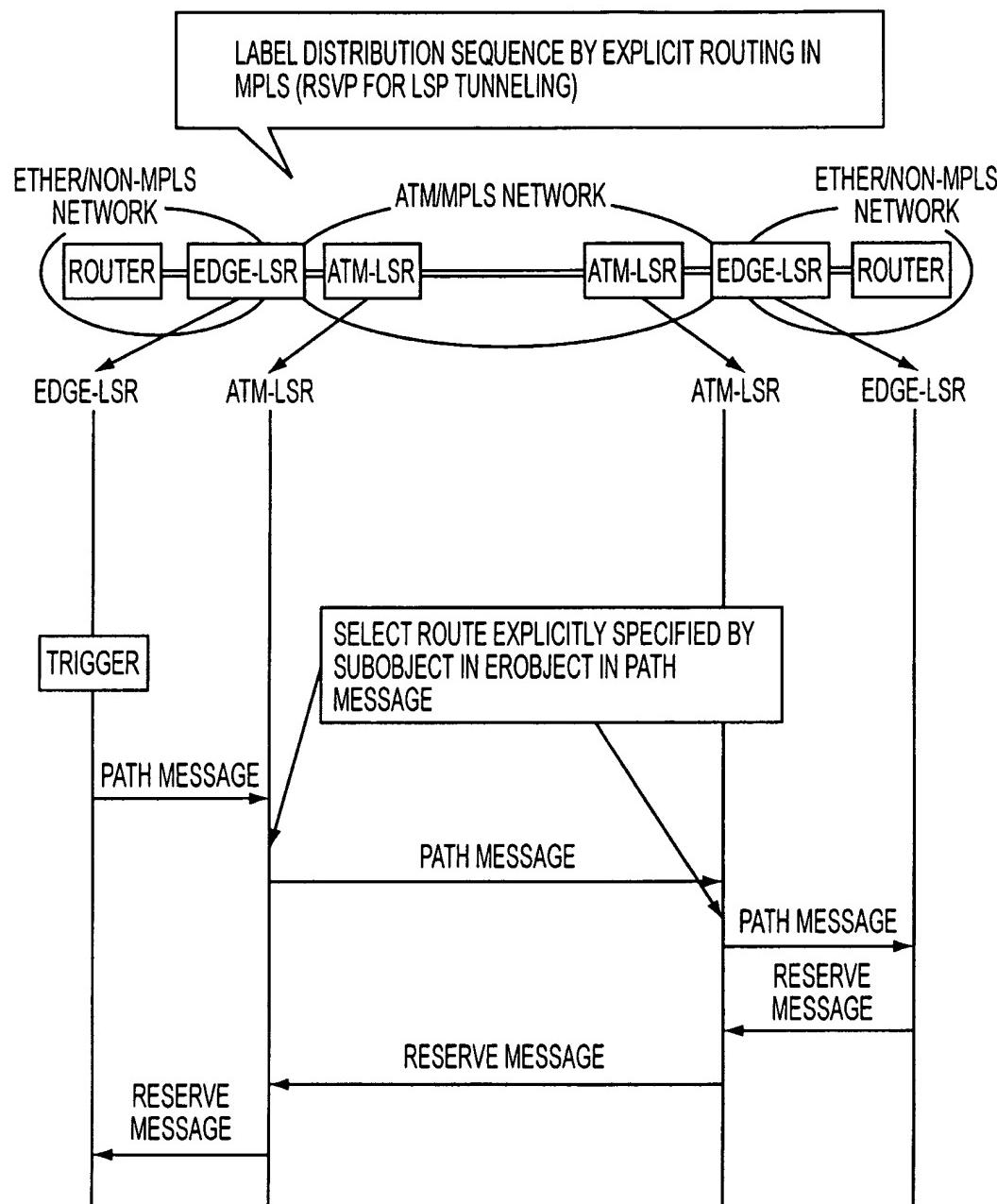
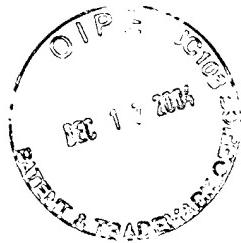


FIG. 7  
PRIOR ART

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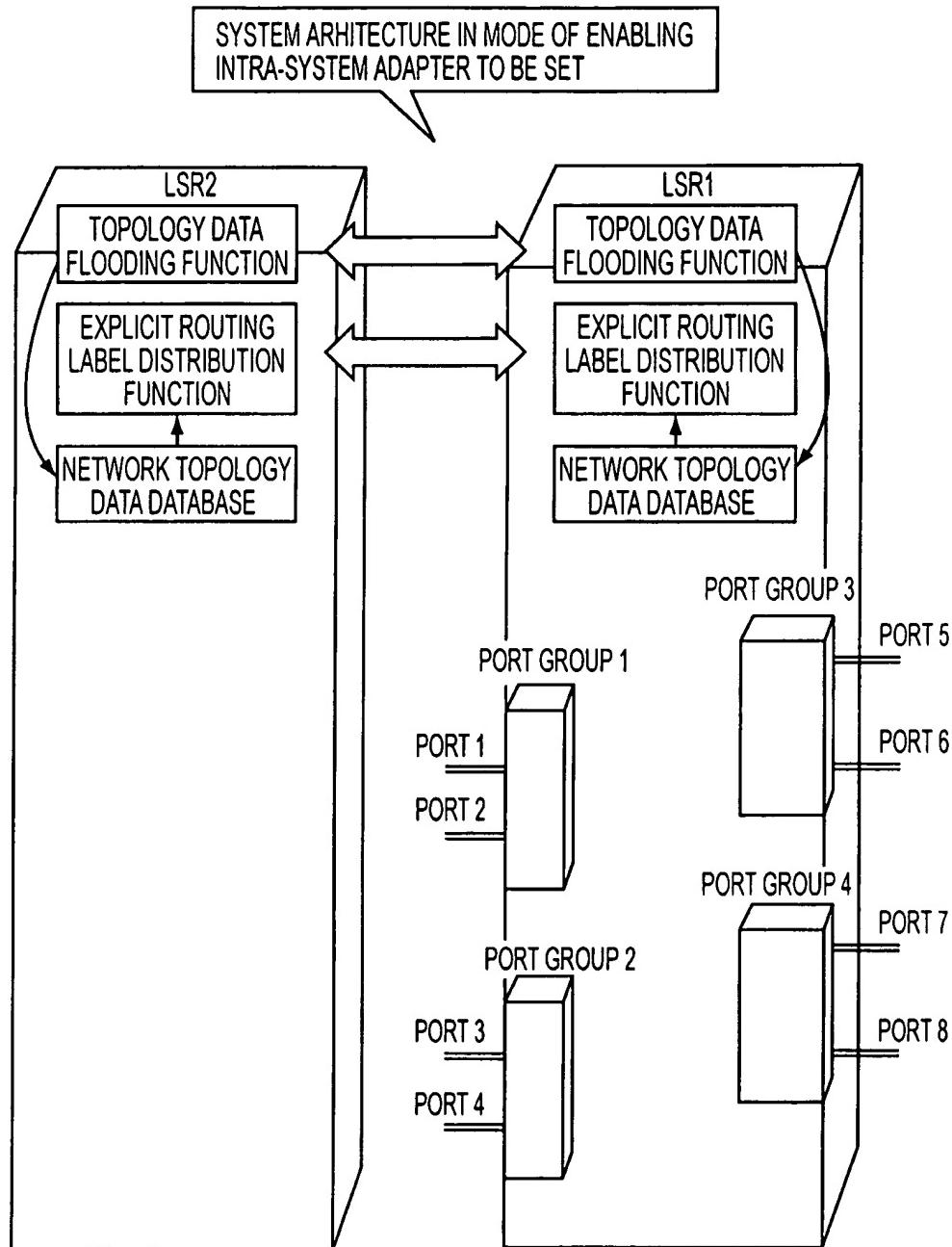
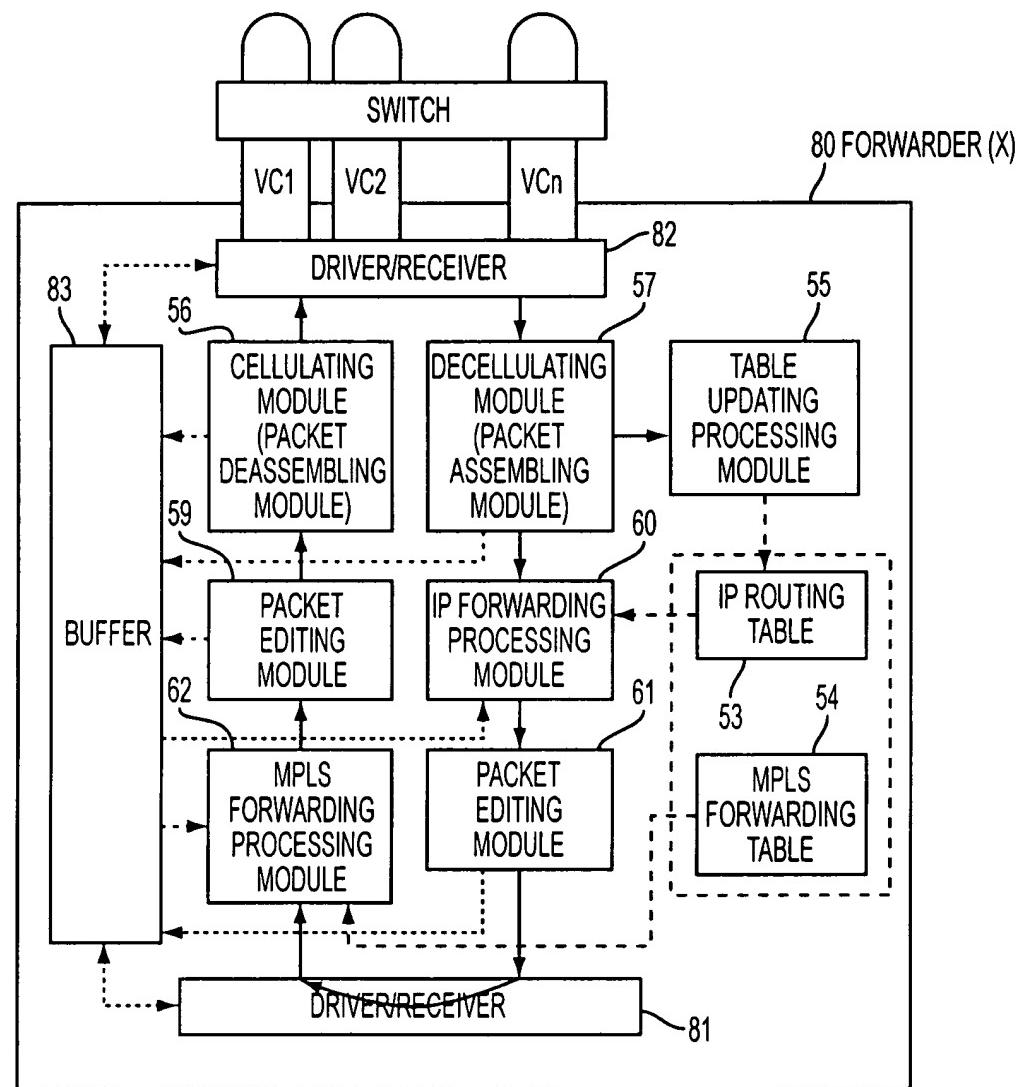


FIG. 11



— : CONTROL  
 - - - : REFERRING/EDITING OF DATA  
 - - - - : REFERRING/UPDATING OF TABLE

FIG. 25